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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/639,753	08/15/2000	Takehiro Ohkawa	NIT-217	4173

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ALEXANDRIA, VA 22314

EXAMINER

YANG, CLARA I

ART UNIT PAPER NUMBER

2635 °

DATE MAILED: 12/15/2003

7

Please find below and/or attached an Office communication concerning this application or proceeding.

Advisory Action

Application No.

09/639,753

Applicant(s)

OHKAWA ET AL.

Examiner

Clara Yang

Art Unit

2635

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 21 November 2003 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

PERIOD FOR REPLY [check either a) or b)]

- a) ☒ The period for reply expires 3 months from the mailing date of the final rejection.
- b) ☐ The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection. ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

1. ☐ A Notice of Appeal was filed on _____. Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.
2. ☒ The proposed amendment(s) will not be entered because:
- (a) ☐ they raise new issues that would require further consideration and/or search (see NOTE below);
 - (b) ☐ they raise the issue of new matter (see Note below);
 - (c) ☒ they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
 - (d) ☐ they present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: _____.

3. ☐ Applicant's reply has overcome the following rejection(s): _____.
4. ☐ Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
5. ☐ The a) ☐ affidavit, b) ☐ exhibit, or c) ☐ request for reconsideration has been considered but does NOT place the application in condition for allowance because: _____.
6. ☐ The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.
7. ☒ For purposes of Appeal, the proposed amendment(s) a) ☒ will not be entered or b) ☐ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: _____.

Claim(s) objected to: _____.

Claim(s) rejected: 1-12.

Claim(s) withdrawn from consideration: _____.

8. ☐ The drawing correction filed on _____ is a) ☐ approved or b) ☐ disapproved by the Examiner.
9. ☐ Note the attached Information Disclosure Statement(s) (PTO-1449) Paper No(s). _____.
10. ☒ Other: See Continuation Sheet

Continuation of 10. Other: On page 10, the Applicant argues that "Hanaoka discloses that the impedance is high in the case of the reset state, and low in the reset release state". It is unclear where this is disclosed in U.S. Patent No. 5,521,590. However, referring to Hanaoka's Fig. 8, Hanaoka's data carrier 200 has a modulator circuit 2 formed by capacitor C3 and a p-channel MOS field effect transistor (FET) T1, which is a depletion FET as indicated by the circuit symbol. When Hanaoka's data carrier 200 is in a reset state, data carrier main circuit 100 no longer provides voltage to transistor T1. In other words, the gate voltage at T1 is zero and T1 is closed (or "on"), thereby causing data carrier 200's impedance state to be low. In addition, due to the presence of diode 21, some level of impedance will always exist even when data carrier 200 is in a reset state; however, the overall impedance level must be higher than the level during the reset state in order to modulate the amplitude of an AC current flowing to coil 1 when data carrier 200 is transmitting data or is in an active state. .

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